Examiner Name Attorney Docket Number

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Application Number		10715459		
Filing Date		2003-11-19		
First Named Inventor	Curtis	Davis		
Art Unit	-	2183		

NVDA/AG-08-0112-US

Keith E. Vicary

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)

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	1	SUZUAZU et al. "A Microprocessor with a 128-Bit CPU, Ten Floating MAC's, Four Floating-Point Dividers, and MPEG-2 Decoder," IEEE Journal of Solid State Circuits, Vol. 34, November 1999, pp. 1608-1618.					
	2	Office	Action, Ser. No. 10/715,400, dated Februa	ary 16, 2010.			
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